	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	18	(pmos and nmos and protection and float$ and gate).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/09 10:52	

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		input same output same buffer same protection same n-well same pmos same control same circuit same.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 18:02
S1	1	("6084431").PN.	USPAT; USOCR	OR	OFF	2006/01/06 17:51
S2	2037	((input adj output) same protection) and buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 17:51
S3	299	S2 and "361"/$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 17:53
S4	227	S3 and pad	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 17:53
S5	227	S4 and protection	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 18:01
S6	1	input same output same buffer same protection same n-well same pmos same control same circuit. clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/06 18:02